


## HiPC 2006 Schedule

 HiPC	Monday, 18 Dec	Tuesday, 19 Dec	Wednesday, 20 Dec	Thursday, 21 Dec
7:00 - 7:30 am		Breakfast		
7:30 - 8:00 am	Breakfast	Opening Remarks (Trinity Hall)	Breakfast	Breakfast
8:00 - 8:30 am		Keynote Address by Jeffrey Wadsworth (Trinity Hall)	Keynote Address by Gene Myers (Trinity Hall)	Keynote Address by Zhiwei Xu (Trinity Hall)
8:30 - 9:00 am	Tutorial 2 (Oak II)	Break	Break	Break
9:00 - 9:30 am	NG:INDIA (Tri I)	Parallel Technical Session I (5 papers) (Tri I)	Parallel Technical Session III (5 papers) (Tri II)	Parallel Technical Session VII (5 papers) (Tri I)
9:30 - 10:00 am	Workshop 1 (Tri II)	Parallel Technical Session II (5 papers) (Tri II)	Parallel Technical Session IV (5 papers) (Vij II)	Parallel Technical Session VIII (5 papers) (Tri II)
10:00 - 10:30 am	Workshop 4 (Vij I)	Lunch	Lunch	Lunch
10:30 - 11:00 am	Workshop 5 (Vij II)	Keynote Address by Pierre Fraigniaud (Trinity Hall)	Parallel Technical Session V (5 papers) (Tri II)	Keynote Address by Dennis Gannon (Trinity Hall)
11:00 - 11:30 am	Break	Break Poster Setup Time	Parallel Technical Session VI (5 papers) (Vij II)	Break
11:30 am - 12:00	Tutorial 2 (Oak II)	Poster Presentations (2-3 min. per poster) (Trinity Hall)	Mini Symposium Part II Industry Session (contd.) (Tri I)	Parallel Technical Session IX (5 papers) (Tri I)
12:00 - 12:30 pm	NG:INDIA (Tri I)	Poster Session (Foyer Area)	Panel Discussion	Parallel Technical Session X (5 papers) (Tri II)
12:30 - 1:00 pm	Workshop 1 (Tri II)	Best Paper (2 papers) and Best Poster Awards Plenary Session (Trinity Hall)	Industry-Academia Panel on Challenges in India for Wider Adoption of HPC Systems (Trinity Hall)	
1:00 - 1:30 pm	Workshop 4 (Vij I)			
1:30 - 2:00 pm	Workshop 5 (Vij II)			
2:00 - 2:30 pm	Tutorial 3 (Oak II)			
2:30 - 3:00 pm	NG:INDIA (Tri I)			
3:00 - 3:30 pm	Workshop 2 (Tri II)			
3:30 - 4:00 pm	Workshop 3 (Vij I)			
4:00 - 4:30 pm	Break			
4:30 - 5:00 pm	Tutorial 3 (Oak II)			
5:00 - 5:30 pm	NG:INDIA (Tri I)			
5:30 - 6:00 pm	Workshop 2 (Tri II)			
6:00 - 6:30 pm	Workshop 3 (Vij I)			
6:30 - 7:00 pm	Tutorial 1 (full-day) - Getting-Going with the Grid and its Applications Canceled			
7:00 - 7:30 pm	Tutorial 2 (half-day) - MPI - Portable Scalable Programming for High Performance Computing		Conference Banquet, Cultural Program, and Dinner	
7:30 - 8:00 pm	Tutorial 3 (half-day) - Cluster and Parallel File Systems for High Performance Computing Clusters			
8:00 - 8:30 pm	NG:INDIA (full-day) - Next Generation Innovations in Network Design and Internet Architecture		Banquet Speech by Harrick Vin (Trinity Hall)	
8:30 - 9:00 pm	Workshop 1 - New Horizons in Compilers			
9:00 - 9:30 pm	Workshop 2 - Many Core Computing			
	Workshop 3 - High Speed DSP Architectures			
	Workshop 4 - Cutting-edge Computing			
	Workshop 5 - Next Generation Wireless Networks			

- Registration desk will be open from 8:00 am to 4:00 pm from Monday to Thursday (December 18 - 21).
- Conference registration fee includes breakfast, lunch, and refreshments from December 18 - 21, and banquet on December 20.
- Conference site: Taj Residency, Bangalore, India.
- Tri - Trinity Hall (Taj Residency), Vij - Vijayanagar Hall (Taj Residency), Oak - Oak Room (Park Hotel, adjacent to Taj Residency).