15TH IEEE INTERNATIONAL CONFERENCE ON HIGH PERFORMANCE COMPUTING







CALL FOR PAPERS

The 15th annual IEEE International Conference on High Performance Computing (HiPC 2008) will be held in Bangalore, India, during December 17-20, 2008. It will serve as a forum to present the current work by researchers from around the world, and act as a venue to provide stimulating discussions and highlight high performance computing activities in Asia. The conference has a history of attracting participation from reputed researchers from all over the world.

HiPC 2008 will focus on the design and analysis of high performance computing and networking systems and their scientific, engineering, and commercial applications. In addition to technical sessions consisting of contributed papers, the conference will include invited presentations, a poster session, tutorials, and vendor presentations. Further, a focused mini symposium will bring together providers and users of HPC to discuss state-of-the-art in HPC platforms and technologies, best practices and experiences. Additional information about the HiPC 2008 conference and HiPC series of meetings is available at http://www.hipc.org.

Authors are invited to submit original unpublished manuscripts that demonstrate current research in all areas of high performance computing including design and analysis of parallel and distributed systems, networking systems, embedded systems, and their applications in scientific, engineering, and commercial areas.

Topics of interest include but are not limited to:

- > High-Performance Computing
- > Parallel and Distributed Algorithms
- > Parallel Languages and Programming Environments
- > Load Balancing, Scheduling and Resource Management
- > Fault-Tolerant Algorithms and Systems
- > Scientific / Engineering / Commercial Applications and Workloads
- > Emerging Applications such as Biotechnology and Nanotechnology
- > Peer-to-peer Algorithms and Networks
- > Heterogeneous Computing
- > Wireless and Mobile Computing
- > Communication/Sensor Networks and Embedded Applications
- > Interconnection Networks and Architectures
- > Scalable Servers and Systems
- > Power-Efficient and Reconfigurable Architectures
- > Compiler Technologies for High-Performance Computing
- > Software Support and Advanced Micro-architecture Techniques
- > Operating Systems for Scalable High-Performance Computing

Best paper awards, sponsored by Infosys, will be given for outstanding contributed papers in two areas: (a) Algorithms and Applications, and (b) Systems. Further details about call for posters, workshops, tutorials, exhibits, as well as submission guidelines are available at the conference website http://www.hipc.org.

IMPORTANT DATES

April 5, 2008 Paper Submission Begins May 12, 2008 Paper Submission Ends May 12, 2008 Workshop Proposals Due May 19, 2008 Tutorial Proposals Due July 14, 2008 Accept/Reject Notification Aug 18, 2008 Camera-Ready Paper Submission Sep 22, 2008 Poster/Presentation Summary Due Nov 07, 2008 Early Registration Deadline

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