



22nd IEEE INTERNATIONAL CONFERENCE ON HIGH PERFORMANCE COMPUTING (HiPC 2015)

December 16-19, 2015

Bengaluru (Bangalore) | www.hipc.org



KEYNOTE SPEAKERS (3rd Speaker to be announced)

◆ TREVOR MUDGE

Bredt Family Professor of Engineering, University of Michigan
THE ARCHITECTURE OF SMART PHONES

◆ RAGHU RAMAKRISHNAN

Head, Big Data Engineering; Head, Cloud Information Services Lab—Microsoft
SCALE-OUT BEYOND MAP-REDUCE

New computing trends are rising to the occasion!

HiPC 2015 will be held at the Park Plaza Bengaluru Hotel in Bengaluru (Bangalore), India from Wednesday, December 16th through Saturday, December 19th. The conference has a history of attracting participation from leading researchers from all over the world and will feature a robust three-day single track technical program of peer reviewed papers and three invited keynote lectures. Other events include a Student Research Symposium, workshops, and academic Birds-of-a-Feather sessions. New in 2015, HiPC will introduce an India-wide Student Parallel Programming Contest. For details on all events, visit the HiPC Website at www.hipc.org.

HiPC 2015 INDUSTRY SUPPORT

HiPC receives strong industry support from companies operating globally and also established in India. In addition to two days of industry exhibits, sponsoring partners hold industry symposiums to bring together providers and users of HPC in a forum for presenting state-of-the-art in HPC platforms and technologies.

General Co-Chairs

- Ramamurthy Badrinath, Ericsson, India
- Rama Govindaraju, Google, USA

Steering Committee Chair

- Viktor K. Prasanna, University of Southern California, USA

Vice General Co-Chairs

- Kalyana Chadalavada, NCSA, USA
- Rajeesh Muralidhar, Intel, India

Program Chair

- Sanjay Ranka, University of Florida, USA

Workshops Co-Chairs

- Manish Parashar, Rutgers University, USA
- Chiranjib Sur, Shell, India

Indian Academia Liaison Chair

- Yogesh Simmhan, IISc, Bangalore, India

Industry Liaison Chair

- Jigar Halani, Wipro, India

Student Research Symposium Co-Chairs

- Kishore Kothapalli, IIT-Hyderabad, India
- Ashok Srinivasan, Florida State University, USA

HiPC 2015 WORKSHOPS

Complementing the main technical program, the HiPC workshops serve to broaden the technical scope of the conference in emerging areas of high performance computing and communication and their applications. HiPC 2015 will host four half-day workshops on December 16th, the first day of the conference. Workshop topics include: Computational Fluid Dynamics (CFD); Big Data; InfoSymbiotics/Dynamic Data Driven Applications Systems (DDDAS); and Architectural Support and Middleware for InfoSymbiotics/DDDAS.

IMPORTANT DATES

Aug 31	Student Competition Submission Deadline
Sep 04	Conference Notification of Acceptance/Rejection
Sep 16	Student Symposium Submissions Due
Sep 30	Student Competition Final Notifications
Oct 16	Author Registration Deadline
Nov 14	Early Registration Deadline



Co-Sponsored by
IEEE Computer Society Technical Committee
on Parallel Processing (TCPP)
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