HiPC 2008 Student Research Symposium

Wednesday, December 17, 2008

8:30 am - 8:40 am
Opening remarks

8:40 am - 9:35 am
Talk by Akshay Kadam, Intel Research
Title: TBA

9:35 am - 10:30 am
Talk by Manish Gupta, Associate Director, IBM India Research Laboratory
Title: TBA

10:30 am - 11:00 am
Break

11:00 am - 1:00 pm
Student Presentations

1:00 pm - 2:00 pm
Lunch

6:30 pm - 8:00 pm
Student Research Symposium Reception

- 6:30 pm - 7:00 pm
  Speech by Vishwanath (Vish) Madhugiri, General Manager and Head of Global Research Alliances, Infosys Technologies Limited
  Title: Co-Creation, Strategic Research and Innovation at Infosys Technologies Limited
- 6:30 pm - 8:00 pm
  Student Poster Exhibits

List of Student Papers

Executing Long-running Multi-component Applications on Batch Grids
Sivagama Sundari Murugavel, Sathish Vadhiyar, and Ravi Nanjundiah, Indian Institute of Science, India

Using Statistical Models for Embedded Java Performance Analysis
Pradeep Rao and Kazuaki Murakami, Kyushu University, Japan

Market-Oriented Meta-Scheduling for Utility Grids
Saurabh Garg, Srikumar Venugopal, and Rajkumar Buyya, The University of Melbourne, Australia

A Performance and Productivity using MPI, Titanium, and Fortress
Chris Bryan and Amy Apon, University of Arkansas, USA, and Wesley Emeneker, Arizona State University, USA

Compiling Irregular Accesses for the Cell Broadband Engine
Pramod Bhatotia, Sanjeev Aggarwal, and Mainak Chaudhuri, IIT Kanpur, India

Fast Floating Point Compression on the Cell BE Processor
Ajith Padyana, Siva Kumar, and Pallav Baruah, Sri Sathya Sai University, India

Adaptive Block Pinning for Multi-core Architectures
Rakesh Kumar and Nitin Chaturvedi, Birla Institute of Technology & Science, Pilani, India

Exploring Software Cache for Cell Processor
Sasikanth Gudla, Pallav Baruah, and Ganapathy Raja Chockalingam, Sri Sathya Sai University, India

हाइपीसी
HiPC

15TH INTERNATIONAL CONFERENCE ON HIGH PERFORMANCE COMPUTING

December 17-20, 2008 | Bangalore, INDIA

http://www.hipc.org

List of Student Papers

A QoS-Based Self-Adaptive Scheduling Algorithm for Real-Time Tasks on Heterogeneous Clusters
Xiaomin Zhu, Fudan University, China

Praana: A Personalized Desktop Filesystem
Amit Roy, and Sundar Balasubramaniam, Birla Institute of Technology & Science, India

Fault Tolerance in Multicore Processors using Reconfigurable Hardware Unit
Rajesh Shanmugam, Vinoth Chandramohan, Srivatsan R, Shanthi Muthusamy, and Harini Sriraman, Anna University, Chennai, India

Design-space exploration of flash augmented architectures
Thanumalayan Sankaranarayana Pillai, Vijay Chidambaram, Ranjani Parthasarathi, CEG, Anna University, India

Performance Metrics of a Parallel Three Dimensional Two-Phase DSMC Method
Benzi John and Murali Damodaran, Nanyang Technical University, Singapore

Evaluating Trust in a Grid Environment
Shashi Bhanwar and Seema Bawa, Thapar University, India

Fault Tolerance in OpenSPARC Multicore Architecture Using Core Virtualization
Kavitha Chandrasekar, Ranjani Parthasarathi, Revathi Ananthachari, and Sangeetha Seshadri, Anna University, India

A Cluster-Based Hierarchical Approach for Scheduling the Mobile Element in Wireless Sensor Networks
K. Indra Gandhi, D. Rajasekar, and Prabu Shyam Mayavaram Mahalingam, Anna University, India

Parallelizing Breadth First Search Using CELL BE
Rahul Gayatri and Pallav Baruah, Sri Sathya Sai University, India

Design and Implementation of a Scalable, Fault tolerant, Heterogeneous and Secured Distributed Storage Framework
Jerre Louis Joney and Aravindan Chandrabose, SSN College of Engineering, India

High throughput design & implementation of multi- FFT/IFFT core in FPGA for hardware acceleration
Hassan Raza and Rajendra Patrikar, Visvesvaraya National Institute of Technology, India

Distributed Algorithms for Maximizing the Network Lifetime in Wireless Sensor Networks
Akshaye Dhawan, Georgia State University, USA

Comparison Study of the MPI Communication Primitives on a Cluster
Arnab Sinha, National Institute of Technology, Durgapur, India, and Nabanita Das, Indian Statistical Institute, India