HiPC 2008 Workshops
Wednesday, December 17, 2008

Workshop I
Workshop on Grid and Utility Computing
Organizers: Anirban Chakrabarti, Infosys Technologies, India; Shubhashis Sengupta, Oracle, India

8:30 am - 10:30 am
Opening Remarks
Shubhashis Sengupta

Keynote Speech
Arvind Jain, System Infrastructure Division, Google

Flexible Management of Business Grids
P. Mckee and M.A. Fisher, Centre for Information Systems and Security Research, UK

A Hierarchical Dynamic Scheduler for Grid Workflow Applications
Nitin B. Gorde and Sanjeev K. Aggarwal, IIT Kanpur, India

11:00 am - 1:00 pm
Dynamic and Integrated Access to Heterogeneous Shared Resources
Jaroslaw Slawi´nski, Magdalena Slawi´nska and Vaidy Sunderam, Emory University, USA

A Provenance-integration Framework for Distributed Workflows in Grid-Environments
Jing Zhao, Fan Sun, Carlo Torniai, Amol Bakshi and Viktor Prasanna, University of Southern California, USA

Generation of Consistent copy of a Database within Grid Computing Environment using a Fuzzy Dump
Subhash Bhalla, University of Aizu, Japan

Distributed Genetic Algorithms on Grid for Protein Structure Prediction
Janaki Chintalapati, Swapna Gunda, Mangala N, Prahlada Rao B. B. and Sundararajan V, C-DAC, India

Disaster management and Assessment System using Interfaced Satellite and Terrestrial Grids
Ojha et al, CDAC
Pankaj Ojha, Mangala N, Prahlada Rao B. B., and Manavalan R., C-DAC, India;
Tapan Mishra, V.Manavala Ramanujam and Haresh Bhat, ISRO, India

Workshop II
Workshop on Service-Oriented Engineering and Optimizations
Organizers: Badrinath Ramamurthy, HP India; Geetha Manjunath, HP India

8:30 am - 10:30 am
Invited Talk
Anantharman. P N, Director - Engineering, Adobe Systems Inc. Bangalore

Towards a Meta-Negotiation Architecture for SLA-Aware Grid Services
Ivona Brandic, Vienna University of Technology, Srinivas Venugopal, Michael Mattess and
Rajkumar Buyya, Univ of Melbourne

A Privacy Framework for Composite Web Services
Ibrahim Al-Nedhami, Univ of Pune and Pradeep K Sinha, C-DAC, Pune

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11:00 am - 1:00 pm
A Distributed Aspect Model for Composite Service
Kumaravel Ganesan, MGR Univ, Chennai; Swarup Mohalik, GM, Bangalore; Cyril Raj, MGR Univ, Chennai

Synthesis and Partial Execution of Service Processes for Matchmaking
Rajesh Thiagarajan, Wolfgang Mayer and Markus Stumptner, Univ of South Australia

Engineering Well Formed Services
Prithviraj Sankaran, Satyam, Chennai

GARUDA: A Case Study of Service Oriented Application Integration Framework
Raghu Anantharangachar, C Nagarjuna Reddy and Doraiswamy Ranganathan, Hewlett-Packard, Bangalore

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Workshop III
High Performance FPGA/Reconfigurable Computing
Tirumale Ramesh, The Boeing Company, USA; Venkata Ramana, Hinditron-CRAY, India

8:30 am - 10:30 am
Welcome and Introduction
Tirumale Ramesh, The Boeing Company, RC Workshop Chair

Redefining Hardware Specializations at Runtime on ASIC/SoC Platforms for Application Acceleration
Invited Talk By Prof SK Nandy, Indian Institute of Science, India

FPGA based reconfigurable digital signal processors for ISRO's spaceborne microwave radars
Shalini Gangele, Ramesh Gameti, R. Senthil Kumar, B. Saravanakumar, Ritesh Kumar Sharma, J.G. Vachhani and N.M. Desai, Space Applications Center, ISRO, India

Balancing coarse-grained pipelined architectures using multiple clock domains on platform FPGA
Sirisha Nalmela, Govindarajulu Regeti, IIT, Hyderabad, India; Chidamber Kulkarni, Xilinx India

11:00 am - 1:00 pm
Ubiquitous Reconfigurable Computing for Net-Enabled Capabilities in Commercial and Military Systems: Technology, Social, and Economic Perspectives
Invited Talk By Tirumale Ramesh and Ken Cureton, The Boeing Company, USA

Data Acquisition, Formatter and Control Unit for Megha Trophies Project
Rahul Dhingani, Madhav Das, B.S. Raman, R. Neelakantan and V.R.Gujraty, Space Applications Center, ISRO, India

FPGA implementation of run-time partial reconfigurable architectures for voice enhancement in data networks
J.K. Jawahar and V. Vaidehi, Anna University, Chennai, India

Integer sequence window based reconfigurable FIR filters
Arul Rajan and H.S. Jamadagni, Indian Institute of Science, India;
Ashok Rao, JSS College of Arts, Commerce and Science, Mysore, India

Short Technology & Applications Trend Discussions and Closing Remarks
Moderator, Tirumale Ramesh, The Boeing Company, RC Workshop Chair

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Workshop IV
Workshop on Next Generation Wireless Networks
Organizers: B. Prabakaran, University of Texas, Dallas, USA;
S. Dharmaraja, Indian Institute of Technology, Delhi, India

2:00 pm - 6:30 pm
TCP Congestion Window Cut Policy for Transmission Errors
M. Alnuem, J. Mellor and R. Fretwell, School of Informatics, University of Bradford, UK.

IPTV Service over IP Multimedia Subsystem
Md Zahid, Arshad Iqbal, Mohammed A Qadeer, Aligarh Muslim University, India

Workshop V
Thermal and Power Management of Emerging Computer Systems - From CPU to Datacenters
Organizers: Venkat Natarajan (Chair), Intel Corporation; Anand Deshpande (Co-Chair), Intel Corporation

2:00 pm - 4:00 pm
Platform-level Thermal and Power Management
Dr. Vivek De Intel Fellow, Corporate Technology Group, Director, Circuit Technology Research

4:30 pm - 6:30 pm
Energy Efficient Datacenters
Prof. Thomas Wenisch, Department of Computer Science, University of Michigan, Ann Arbor

Optimizing Multicore/Manycore infrastructure applications for power savings through reduced run times
Software Demo by Intel India Team