



हाइपीसी HiPC

17TH INTERNATIONAL CONFERENCE ON HIGH PERFORMANCE COMPUTING

December 19-22, 2010 | Goa, INDIA

CALL FOR PAPERS

The 17th annual IEEE International Conference on High Performance Computing (HiPC 2010) will be held in Goa, India, during December 19-22, 2010. It will serve as a forum for researchers from around the world to present their current research efforts and findings, and will act as a venue for stimulating discussions and for highlighting high performance computing (HPC) activities in Asia. The conference has a history of attracting participation from reputed researchers from all over the world.

HiPC 2010 will focus on the design and analysis of high performance computing and networking systems and their scientific, engineering, and commercial applications. In addition to technical sessions consisting of contributed papers, the conference will include invited presentations, a student research symposium, tutorials, and vendor presentations. Additional information about the HiPC 2010 conference and HiPC series of meetings is available at <http://www.hipc.org>.

Authors are invited to submit original unpublished manuscripts that demonstrate current research in all areas of high performance computing including design and analysis of parallel and distributed systems and embedded systems, and their applications in scientific, engineering, and commercial areas.

Topics of interest include but are not limited to:

- > High-Performance Computing
- > Parallel and Distributed Algorithms
- > Parallel Languages and Programming Environments
- > Load Balancing, Scheduling and Resource Management
- > Fault-Tolerant Algorithms and Systems
- > Scientific/Engineering/Commercial Applications and Workloads
- > Emerging Applications such as Biotechnology and Nanotechnology
- > Cluster and Grid Computing
- > Peer-to-peer Algorithms and Networks
- > Heterogeneous Computing
- > Wireless and Mobile Computing
- > Communication/Sensor Networks and Embedded Applications
- > Interconnection Networks and Architectures
- > Scalable Servers and Systems
- > High Performance/Scalable Storage Systems
- > Power-Efficient and Reconfigurable Architectures
- > Compiler Technologies for High-Performance Computing
- > Software Support and Advanced Micro-architecture Techniques
- > Operating Systems for Scalable High-Performance Computing

Best paper awards, sponsored by Infosys, will be given for outstanding contributed papers. Further details about call for student research symposium, workshops, tutorials, and exhibits, as well as submission guidelines are available at the conference website <http://www.hipc.org>.

IMPORTANT DATES

- April 04, 2010 > Conference Paper Submission Begins
- May 15, 2010 > Conference Paper Submission Ends
- May 15, 2010 > Workshop Proposals Due
- May 18, 2010 > Tutorial Proposals Due
- July 17, 2010 > Notification of Acceptance/Rejection
- Aug 17, 2010 > Camera-Ready Paper Submission
- Sep 14, 2010 > Student Symposium Submissions Due
- Oct 19, 2010 > Student Symposium Accept/Reject Decisions
- Nov 09, 2010 > Early Registration Deadline

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Pending Support From

IEEE Computer Society Technical Committee on Parallel Processing (TCPP) • ACM Special Interest Group on Computer Architecture (SIGARCH) • European Association for Theoretical Computer Science • IFIP Working Group on Concurrent Systems • National Association of Software and Service Companies (NASSCOM) • Manufacturers' Association for Information Technology (MAIT) • HiPC Trust

