

# Birds of a Feather Session & Dinner

FUJITSU

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## 19th IEEE International Conference on High Performance Computing

December 18-21, 2012 | Pune, INDIA | [www.hipc.org](http://www.hipc.org)

On behalf of the IEEE Computer Society, Fujitsu India, platinum sponsor of HiPC 2012 conference, we are pleased to invite you for the Birds of Feather (BOF) session on 19th Dec '12 followed by dinner.

### Agenda highlights for the Fujitsu Birds of a Feather session

Introduction to 'challenges to achieve real Petascale computing in Japan' from various aspects.

- Current status and trends of HPC in Japan including K computer, future project and Fujitsu's involvement
- Development of K computer (design concept, datacenter environment, performance, etc.)
- HPC application topics (i.e. Tsunami simulation.)

हाइपीसी  
HiPC



INTERNATIONAL CONFERENCE ON  
HIGH PERFORMANCE COMPUTING

**Birds of Feather**

**19 December 2012**

**7 p.m. to 9 p.m**

**Le Meridian, Pune**

**Majestic 1 Hall**

shaping tomorrow with you

# Speakers Representing Fujitsu

## Talk 1:

**Dr. Motoi Okuda,**  
Fujitsu Ltd

**Title:** Recent Japanese challenge for High Performance Computing - K computer and future

**Abstract:** Japanese government are strategically expanding investment to HPC. The talk aims to cover the outline of the ongoing Japanese HPC project, the K computer project and HPCI : HPC application project. Japanese government has just begun their new challenge to the future supercomputer system. The speaker will also refer to the outline of Japanese activities, based on lessons we learned from K Computer project

**Duration:** (40min. with 5 min Q&A, including K computer making video)

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## Talk 2:

**Takayuki Hoshiya,**  
Fujitsu Ltd

**Title:** Introduction of the K computer and its successor PRIMEHPC FX10

**Abstract:** K computer is the first 10PFLOPS system and PRIMEHPC FX10 is its successor developed by RIKEN and Fujitsu. In this talk, the speaker will introduce key technologies for petascale computer and how they are implemented in K computer and FX10. SPARC64 VIIfx, IXfx processor had extended SPARC architecture called HPC-ACE. It's concepts and performance in HPC application would be presented. Also petascale system configurations, system software for using huge system effectively, and technologies to achieve lowpower consumption and high reliability would be covered.

**Duration:** (40min. with 5 min Q&A)

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## Talk 3:

**Dr Nick Wilson**  
(Fujitsu Laboratories  
of Europe)

**Title:** HPC Research and Open Innovation in Fujitsu Laboratories of Europe

**Abstract:** High-performance computing (HPC) is a vital tool in ensuring a safe and prosperous future society. Fujitsu Laboratories of Europe (FLE) is using open innovation to develop software technologies that increase the effectiveness of HPC applications on current and future supercomputers. Examples include the Open Petascale Libraries project, which promotes the development of numerical libraries for massively parallel computers based on multi-core nodes; work with The Australian National University targeted at fault-resilient software; and the HPC Wales project, which seeks to accelerate industrial competitiveness through use of HPC in small and medium enterprises. Examples will also be given of two recent HPC application projects in FLE: in silico drug testing, which will contribute to cost-effective healthcare, and tsunami modelling to mitigate the effects of future natural disasters.

**Duration:** (40min. with 5 min Q&A )

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### Mr. Akihiko Fujino

Since joining Fujitsu in 1987, he has been in charge of marketing, promotion of business and local business support for high performance computer mainly super computer. Now, he is Director in Business Strategy & Planning Office, Technical Computing Solutions Unit promoting domestic and international supercomputer business.

### Mr. Hoshiya Takayuki

Joined Fujitsu Limited in 1985 and assigned to Display device laboratory in Fujitsu laboratory. He was also Engaged in hardware design for LCD controller development. In 1993, he was assigned to Software Division in Information Systems Group in Fujitsu limited, Engaged in system software development for super computer system and in 2001, he was Assigned as a Manager, Server software Div. in Computer Systems Group and is currently engaged in system software development for super computer system.

### Dr. Motoi Okuda

Motoi Okuda is currently an Executive Architect, Technical Computing Solutions Unit, Fujitsu Ltd. He has vast experience in Supervising planning, marketing, and support activities of High Performance Computer business. His has Ph.D in Information Science from Japan Advanced Institute of Science and Technology, Ishikawa and Master of Nuclear Engineering from Nagoya University. He is also part of Executive board members in The Japan Society for Computational Engineering and Science (JSCES).

### Mr. Nicholas Wilson

Nicholas Wilson is currently working as Principal Researcher, Technical Computing Research Division in Fujitsu Laboratories of Europe. Nicholas obtained PhD in theoretical chemistry from the University of Birmingham in the UK in 2000. After postdoctoral positions at several UK universities Nicholas joined Fujitsu in 2006. Nicholas specialize in performance optimization of scientific applications and the porting of scientific software to new architectures.

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TO REGISTER CONTACT

Fujitsu India Pvt. Ltd.  
email: [marketing-india@ts.fujitsu.com](mailto:marketing-india@ts.fujitsu.com)  
Toll free No: 1800 102 3457