HiPC 2016 CONFERENCE OVERVIEW

Starting on Monday, December 19th, the first day of HiPC 2016 will feature four workshops and conclude with an Industry Gala. The main technical program on Days 2, 3 and 4 will showcase three keynote speakers and seven single track sessions of forty peer reviewed papers from all over the world covering important and timely topics in all areas of high performance computing, data, and analytics. On Days 2 and 3, there will be a full program of industry exhibits and related industry events including Industry, Research and Users Symposium (IRUS) sessions to provide a forum for presenting state-of-the-art in HPC platforms and technologies, discussing best practices, and exchanging experiences. In addition, industry representatives will host BoF sessions on these two days.

HiPC 2016 KEYNOTE SPEAKERS

Genomes Galore: Big Data Challenges in the Life Sciences
Srinivas Aluru, Georgia Institute of Technology, USA

China's HPC development in the next 5 years
Depei Qian, Sun Yat-sen University and Beihang University, China

Toward Extreme-Scale Processor Chips
Josep Torrellas, University of Illinois Urbana-Champaign, USA

HiPC 2016 WORKSHOPS

→ **BigDF: Foundations of Big Data Computing**

→ **CFD: Computational Fluid Dynamics**

→ **HBME: High Performance Computing and Big Data in Molecular Engineering**

→ **SCI: Software Composable Infrastructure**

HiPC 2016 ORGANIZATION

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Kalyana Chadalavada, Intel, USA

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STUDENT PROGRAMS

On Days 2 and 3, posters of work selected for the Student Research Symposium will be on display. As well, the conference will host Intel and NVIDIA sponsored Student Parallel Programming Challenges.

Co-Sponsored by IEEE Computer Society Technical Committee on Parallel Processing (TCPP) & HiPC Education Trust

TITANIUM

PLATINUM

GOLD